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Bib Data Sheet

CONFIRMATION NO. 5191

SERIAL NUMBER 10/782,450	FILING DATE 02/19/2004  RULE	CLASS 716	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. 400.184US05
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## APPLICANTS

Christophe Chevallier, Palo Alto, CA;

Adriana Ababei, Santa Clara, CA;

## \*\* CONTINUING DATA \*\*\*\*\* PD

This application is a DIV of 10/199,727 07/19/2002 PAT 6,769,103

## \*\* FOREIGN APPLICATIONS \*\*\*\*\* PD

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 05/14/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY CA	SHEETS DRAWING 4	TOTAL CLAIMS 18	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <i>Paul</i> Examiner's Signature	<i>Pinh</i> Initials			

## ADDRESS

27073

LEFFERT JAY &amp; POLGLAZE, P.A.

P.O. BOX 581009

MINNEAPOLIS, MN

55458-1009

## TITLE

Identifying line width errors in integrated circuit designs

FILING FEE  RECEIVED 770	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue )
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